

FIG. 1

Engineering Block 10

✓ 21

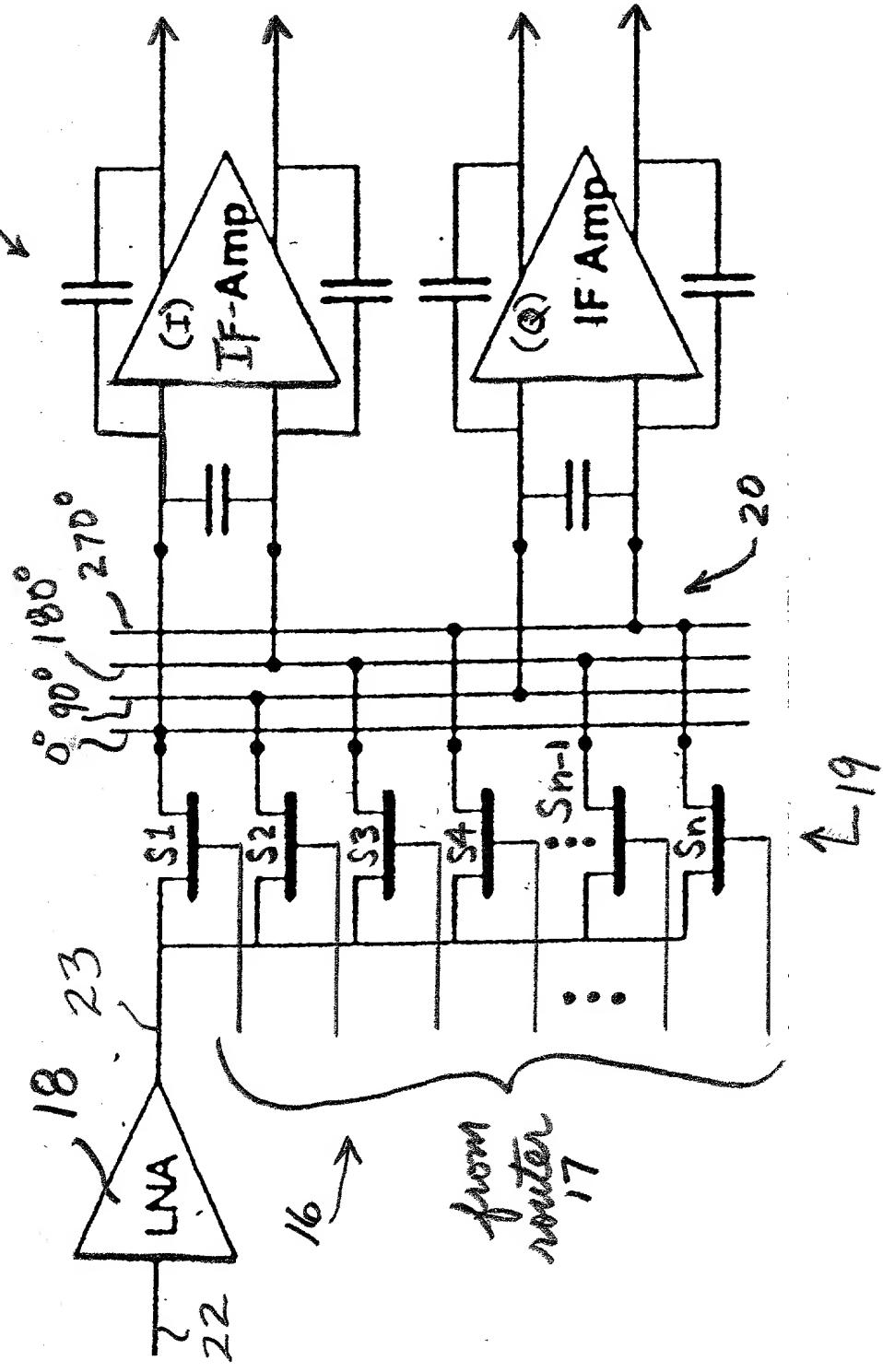


Fig. 2

✓ 19

✓ 20

W G S I C D : H E E R E E E E E

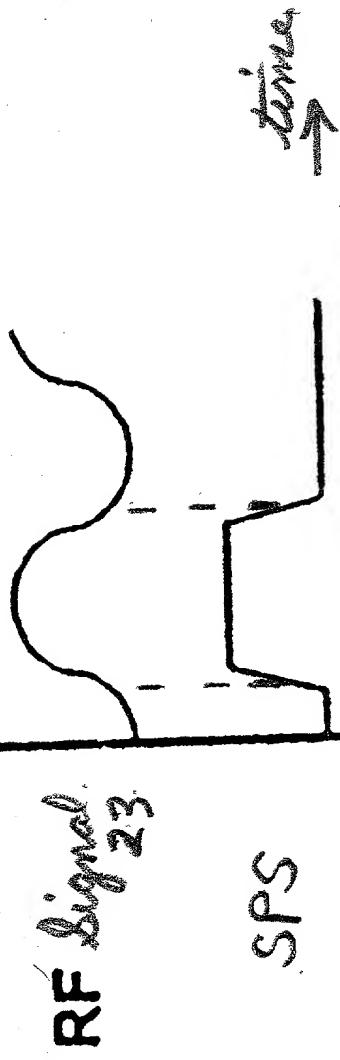


Fig. 3

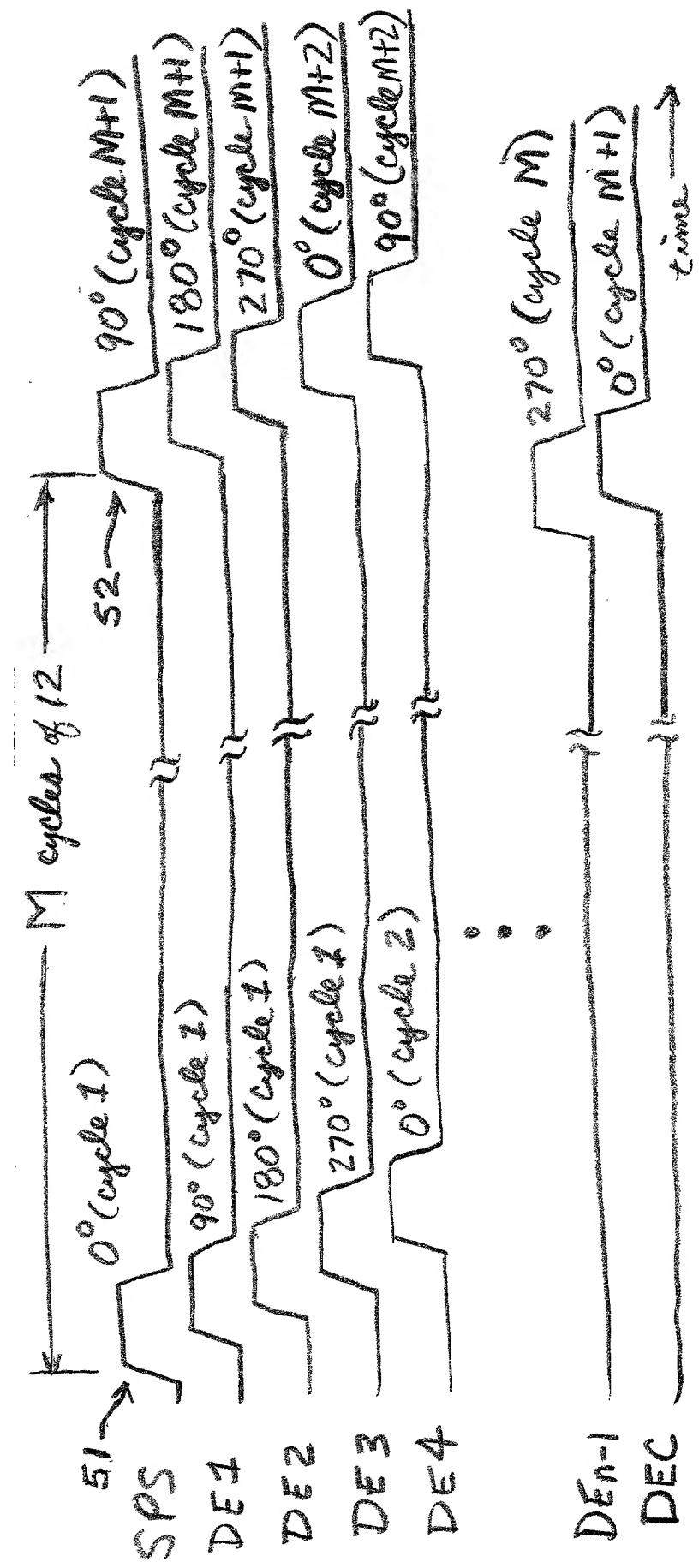


Fig. 5

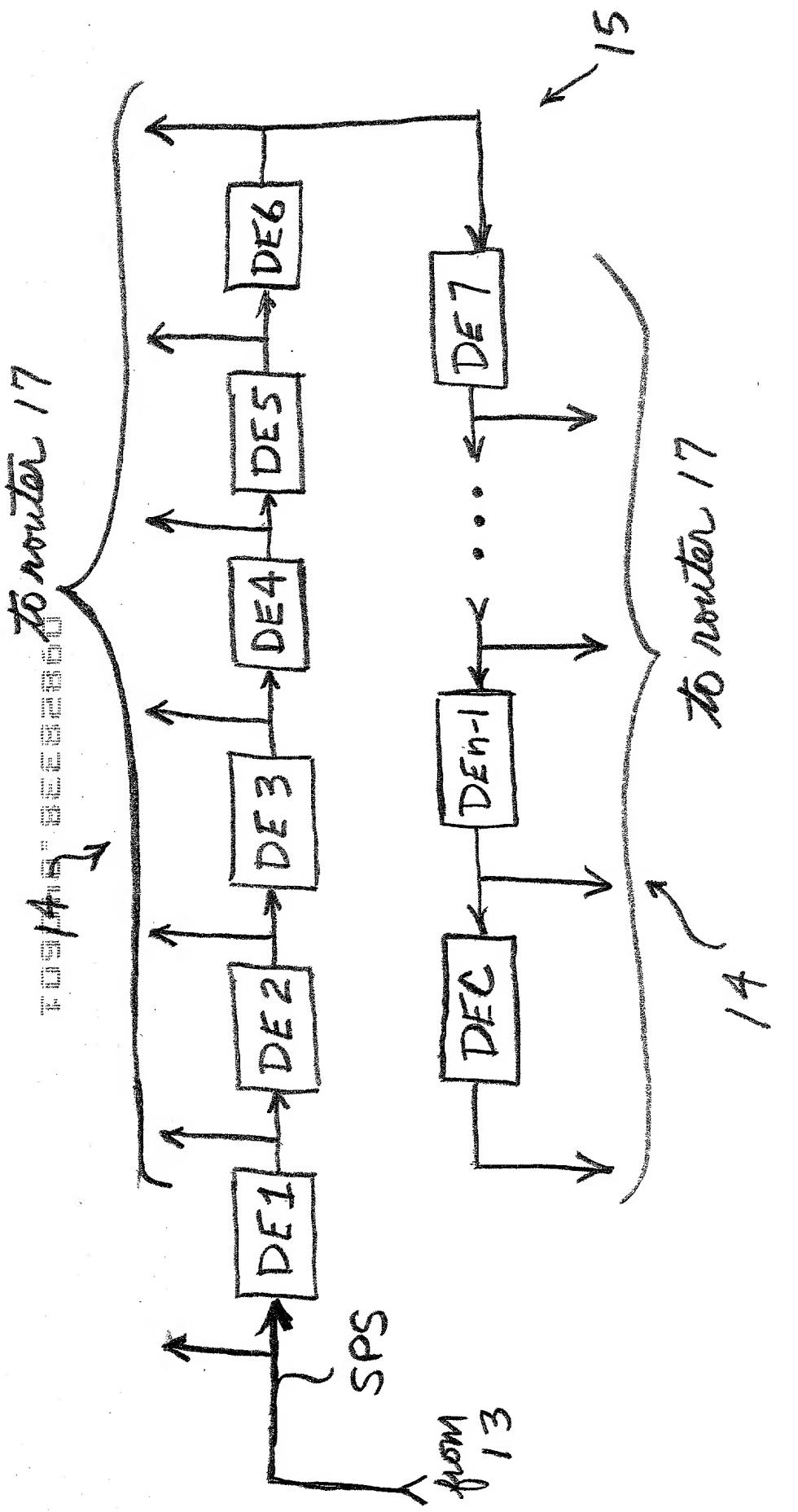


FIG. 4

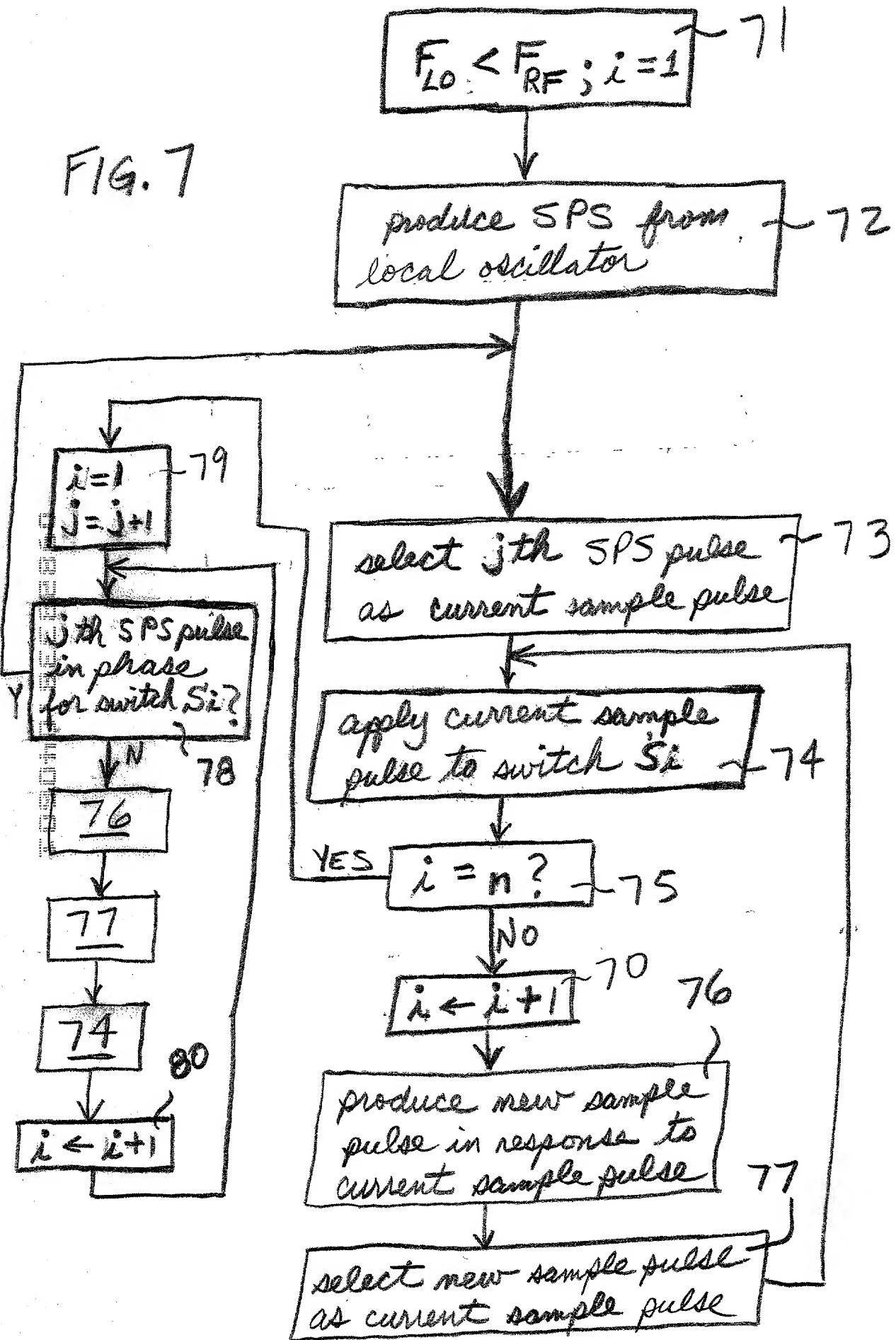
Switch (Phase)	RF pulse source cycle							
	RF cycle	Pulse source						
S1 (0)	K	SPS	K+4	DEC	K+8	DEC	K+12	DEC
S2 (90)	DE1	SPS	DE1	SPS	DE1	SPS	DE1	SPS
S3 (180)	DE2		DE2		DE2		DE2	
S4 (270)	DE3		DE3		DE3		DE3	
S5 (0)	DE4		DE4		DE4		DE4	
S6 (90)	DE5		DE5		DE5		DE5	
S7 (180)	DE6		DE6		DE6		DE6	
S8 (270)	DE7		DE7		DE7		DE7	
S9 (0)	DE8		DE8		DE8		DE8	
S10 (90)	DE9		DE9		DE9		DE9	
S11 (180)	DE10		DE10		DE10		DE10	
S12 (270)	DE11		DE11		DE11		DE11	
S13 (0)	DE12		DE12		DE12		DE12	
S14 (90)	DE13		DE13		DE13		DE13	
S15 (180)	DE14		DE14		DE14		DE14	
S16 (270)	DE15		DE15		DE15		DE15	

FIG. 6

to cycle K

K=K+16

FIG. 7



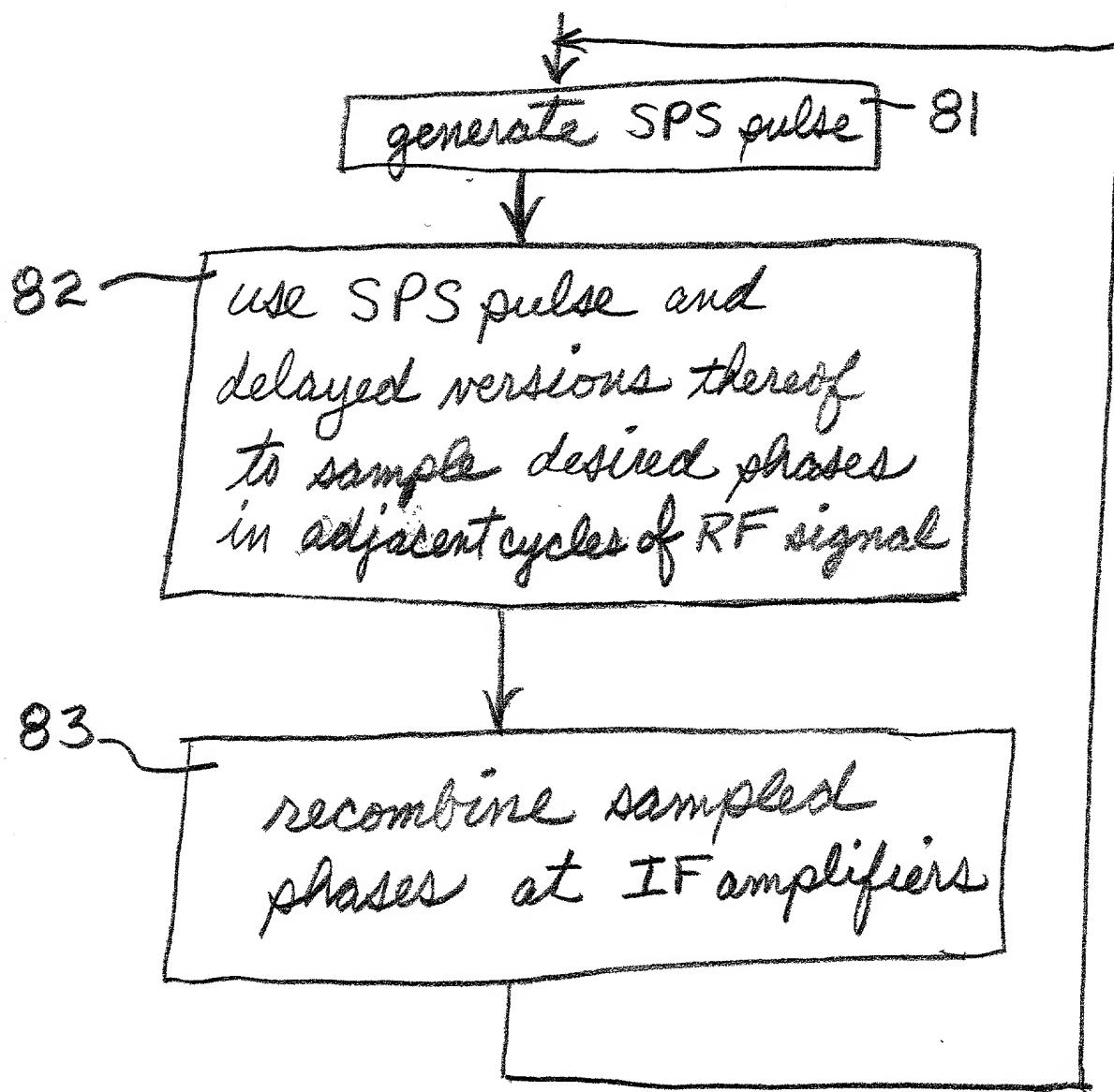


FIG. 8

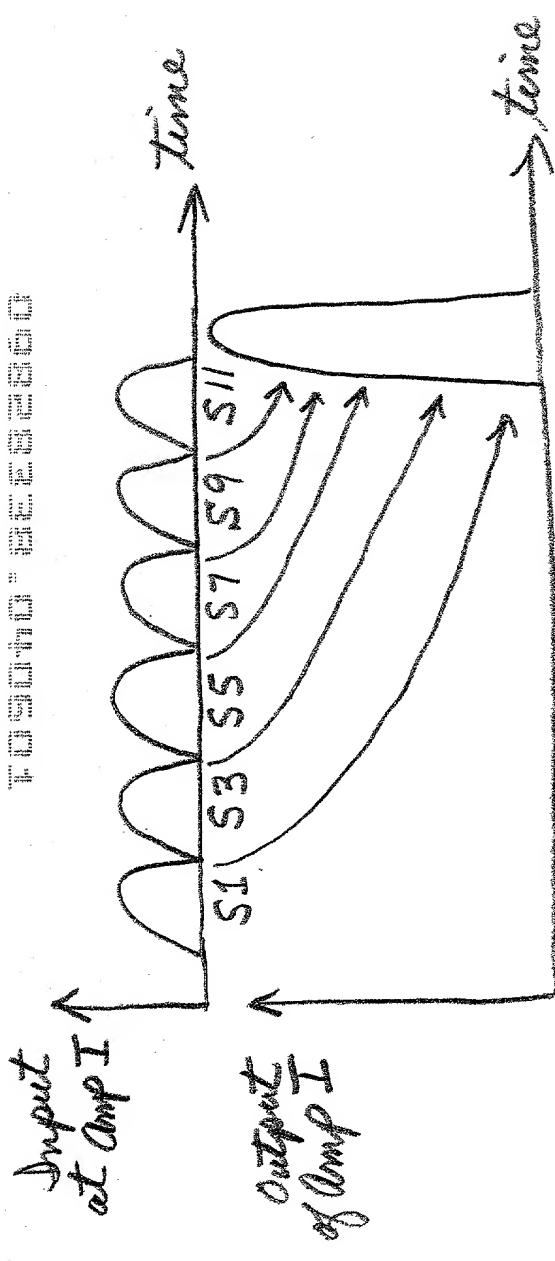


Fig. 9

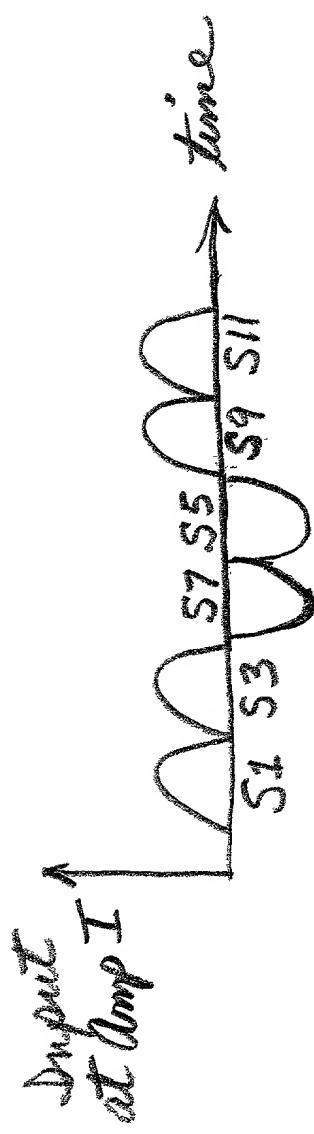


Fig. 10